



In re Application of: Yamamoto, et al.
Serial No.: 09/600,931
Confirmation No.: Unassigned
Filed: July 21, 2000
For: DAMASCENE INTERCONNECTION AND
SEMICONDUCTOR DEVICE

COMMISSIONER FOR PATENTS
Washington, DC 20231

Sir:

Transmitted herewith is an Amendment in the above-identified application.

Small entity status of this application under 37 C.F.R. 1.9 and 1.27 has been established by a verified statement previously submitted.

A verified statement to establish small entity status under 37 C.F.R. 1.9 and 1.27 is enclosed.

No additional fee is required.

The fee has been calculated as shown below:

	(Col. 1)		(Col. 2)		(Col. 3)
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA
TOTAL	* 12	MINUS	** 20	= 0	
INDEP.	* 2	MINUS	*** 3	= 0	

SMALL ENTITY		OR	OTHER THAN A SMALL ENTITY	
RATE	ADDL. FEE		RATE	ADDL. FEE
x 9=	\$		x 18=	\$
x 40=	\$		x 80=	\$
x 135=	\$		x 270=	\$
TOTAL	\$ 0.00		TOTAL	\$ 0.00

FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS

Please charge my Deposit Account No. 08-2461 in the amount of \$ _____. A duplicate copy of this sheet is attached.

A check in the amount of \$ _____. is attached.

The Commissioner is hereby authorized to charge any fees or additional fees associated with this communication or credit any overpayment to Deposit Account No. 08-2461. A duplicate copy of this sheet is attached.

Any filing fees under 37 C.F.R. 1.16 for the presentation of extra claims.

Any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted,

Rod S. Turner

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Attorney Docket No. 362-43 PCT/US

I hereby certify this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to:
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20231 on March 7, 2002

Signature: Joyce Peterson

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Yamamoto, et al. Examiner: Eugene Lee
Serial No.: 09/600,931 Group Art Unit: 2815
Confirmation No.: Unassigned Docket: 362-43 PCT/US
Filed: July 21, 2000 Dated: March 7, 2002
For: DAMASCENE
INTERCONNECTION AND
SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, DC 20231

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20231 on March 7, 2002

Signature: *Joyce Peterson*

REPLY TO FINAL OFFICE ACTION

Sir:

In response to the final Office Action mailed October 9, 2001, a reply to which is due March 9, 2002 with a two-month Petition for Extension of Time submitted herewith, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend Claim 8 by rewriting the same as follows:

8. (Twice Amended) A semiconductor device, comprising:
 - a semiconductor substrate;
 - an insulating film formed on said semiconductor substrate;
 - an interconnection trench formed on said insulating film and communicating with a semiconductor element;
 - a pad trench formed on said insulating film and communicating with said interconnection trench;
 - a protrusion formed by a portion [of] not removed of said insulating film in said pad trench and reducing a substantial opening area of said pad trench;
 - a conductive film buried in said interconnection trench and said pad trench;

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